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3. An efficient BIST method for distributed small buffers 

Jone, W.B.; Huang, D.C.; Wu, S.C.; Lee, K.J.; Very Large Scale Integration (VLSI) Systems, IEEE Transactions on Volume 10, Issue 4, Aug. 2002 Page(s):512 - 515 Digital Object Identifier 10.1109/TVLSI.2002.800532

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Inst. of Comput. Technol., Chinese Acad. of Sci., China Qin Zhang Peng Li Jizhong Han Chengde Han

This paper appears in: Networking, Architecture, and Storages, 2006. NAS '06. International Workshop

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servers or between two servers. Typically, conventional data center uses multiple network adapters to build the cost, and so on. Fortunately, InfiniBand provides a high-bandwidth, Iow-latency network, and it has been widely which has five major features: protocol conversion, bandwidth aggregation, virtual service mapping, dynamic reconfiguration and redundant array. Through verification and evaluation, latency of RGAMF is between 1us networks. Many methods, such as protocol conversion and bandwidth aggregation, have been proposed to server fabric, which bring many drawbacks, such as complex management, hard scalability and expensive and 2us, and the bandwidth of all IP links is close to wire speed at 1Gbps in both directions. Based on the FPGA (field programmable gate array) embedded in each gateway of array, this architecture also provides adopted so that many researches have been focused on connecting between InfiniBand and conventional heterogeneous interconnect architecture called RGAMF (reconfigurable gateway array of multiple fabrics), used in current data center to create a unified fabric. However, fiber channel and IP fabric are still widely In today's data center, the demand on server I/O is increasing to transfer huge data between clients and eliminate performance limitation caused by heterogeneous fabrics. Nevertheless, diversity and dynamic equirements of server I/O on data center still didn't be considered. This paper proposes a novel petter flexibility and scalability.

Index Terms

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bandwidth allocation client-server systems field programmable gate arrays internetworking protocols reconfigurable architectures redundancy http://ieeexplore.ieee.org/search/srchabstract.jsp?arnumber=1654539&isnumber=34686&punumber=11002&k2dockey=1654539... 12/29/06

architecture high-bandwidth network low-latency network protocol conversion reconfigurable gateway array of multiple fabrics redundant array server I/O virtual service reconfiguration fiber channel field programmable gate array heterogeneous interconnect Non-controlled Indexing

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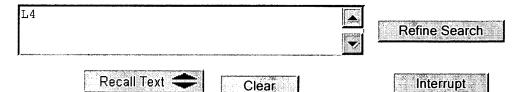
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<u>L3</u>	ll.clm.	40	<u>L3</u>
<u>L2</u>	L1 and (physical same serial\$2)	68	<u>L2</u>
<u>L1</u>	(storage or disk or disc) same redundant same virtual\$7	526	<u>L1</u>

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- L8 L7 and (SVC or PSD or ATA or SCSI)
- L7 15 and L6
- <u>L6</u> 710/240,74,300,316,3,36,38,314,315;711/151,114,154,203;714/5,6,43;370/351,431,464,906,910.
- <u>L5</u> (storage or disk or disc) same redundant same virtual\$7

DB=PGPB; PLUR=YES; OP=OR

- <u>L4</u> L3 and (physical same serial\$2)
- L3 11.clm.
- <u>L2</u> L1 and (physical same serial\$2)
- L1 (storage or disk or disc) same redundant same virtual\$7